

13 Rec'd PCT 28 APR 1993

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#2

PATENT
TESSERA 3.3-018

UNITED STATES DESIGNATED OFFICE (DO/EO/US)

#2

In re International application :
PCT/US91/06920 :
Int. Filing Date: Sept. 24, 1991 :
Priority Date: Sept. 24, 1990 :
Applicant: Igor Y. Khandros, et al. :
Title: SEMICONDUCTOR CHIP ASSEMBLIES, :
METHODS FOR MAKING SAME AND :
COMPONENTS FOR SAME :
: April 28, 1993

Hon. Commissioner of Patents
and Trademarks
Box PCT
Washington, D.C. 20231

Attention: DO/EO/US

SUBMISSION OF EXECUTED DECLARATION, FORMAL
DRAWINGS AND ASSIGNMENT

Sir:

Applicant herewith encloses the original executed Declaration for the above-identified patent application which was forwarded for filing on March 4, 1993. Also enclosed is a check in the amount of \$130.00 to cover the surcharge for filing a Declaration on a date later than the filing date of the application.

In addition, applicant herewith encloses nineteen (19) sheets of formal drawings for the above-identified patent application.

CERTIFICATE OF MAILING BY "EXPRESS MAIL"

"Express Mail" Mailing Label Number TB 153521962 US

Date of Deposit April 28, 1993

I hereby certify that this paper and/or fee is being deposited with the United States Service Express Mail Post Office to Addressee service under 37 C.F.R. 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231


John J. Frazer

Lastly, we are enclosing an Assignment from the inventors to Tessera, Inc. for recording along with a Recordation Form Cover Sheet and a check for \$40.00 to cover the recording fee. Kindly return the recorded Assignment to our office.

If the Examiner has any questions concerning this matter, he is requested to call Applicant's attorney at (908) 654-5000.

Respectfully submitted,

LERNER, DAVID, LITTENBERG,
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